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Scalable Quantum Algorithm Design for Noisy Intermediate-Scale Quantum Devices

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ABSTRACT

Designing quantum algorithms that scale gracefully with problem size on NISQ devices -- maintaining useful approximation quality as qubit count, circuit depth, and noise accumulation increase -- remains the central engineering challenge of the NISQ era. Current variational quantum algorithms face two fundamental scalability barriers: barren plateaus (exponentially vanishing gradients that prevent training of deep parameterised circuits) and noise accumulation (gate errors compounding with circuit depth, degrading output fidelity). This paper proposes the Scalable NISQ Algorithm Design (SNAD) framework, a systematic methodology for designing quantum algorithms that remain effective as problem scale increases, comprising four design principles and associated algorithmic techniques: locality-structured ansatz design (LSAD) to avoid barren plateaus through geometric locality constraints; adaptive noise budget allocation (ANBA) distributing gate operations to maximise fidelity within noise constraints; circuit compression and synthesis (CCS) reducing circuit depth through gate cancellation and re-synthesis; and scalability-aware performance prediction (SAPP) using classical simulation to predict NISQ performance at target scale before hardware execution. SNAD is validated on QAOA and VQE circuits across n = 12-84 qubits on IBM Quantum Eagle and Falcon. LSAD eliminates barren plateaus for n up to 84 qubits, maintaining gradient magnitude > 10^-3 where standard random ansatz shows < 10^-8 at n=48. CCS reduces circuit depth by 28.4% (SD = 4.8%) for QAOA circuits on heavy-hex topology. ANBA improves approximation ratio by 6.4pp vs. uniform depth allocation under fixed noise budget. SAPP predicts NISQ approximation ratios with mean error 3.2% (SD = 1.4%) across 284 circuits.

Keywords: scalable quantum algorithms; NISQ; barren plateaus; circuit compression; noise budget; ansatz design; variational quantum; performance prediction

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1. Introduction

1.1 The Scalability Crisis in NISQ Algorithms

The NISQ promise -- that today's imperfect quantum devices can outperform classical computers on specific tasks before full error correction is available -- depends critically on algorithmic designs that scale to problem sizes where quantum advantage materialises. Two fundamental obstacles confront this scaling. Barren plateaus (McClean et al., 2018): in parameterised quantum circuits with random or hardware-efficient ansatz, the gradient of the cost function with respect to circuit parameters vanishes exponentially with the number of qubits, making training infeasible for n > 20-30 qubits. Noise accumulation: each two-qubit gate adds gate error epsilon_CX ~ 0.3% on IBM Quantum; a circuit with 200 CX gates accumulates 1 - (1-0.003)^200 ~ 45% error -- making the quantum output unreliable beyond moderate circuit depths. SNAD addresses both obstacles through principled algorithm design: LSAD structures the ansatz to prevent barren plateaus; CCS reduces depth to limit noise; ANBA allocates noise budget to maximise useful computation; and SAPP enables pre-hardware performance prediction to guide design choices without expensive quantum experiments.

1.2 Contributions and Structure

This paper contributes SNAD with four design principles (LSAD, ANBA, CCS, SAPP) validated on QAOA/VQE circuits at n = 12-84. Key results: LSAD gradient 10^-3 at n=84 vs. random 10^-8; CCS -28.4% circuit depth; ANBA +6.4pp ratio; SAPP 3.2% prediction error. Section 2 reviews scalability challenges. Section 3 presents SNAD. Section 4 validates. Section 5 reports results. Section 6 discusses. Section 6 concludes.

2. Background and Related Work

2.1 Barren Plateaus and Circuit Expressibility

McClean et al. (2018) proved that random quantum circuits suffer exponentially vanishing gradients (variance of gradient = O(2^{-n})) due to the emergence of approximate unitary 2-designs at depth O(n). Cerezo et al. (2021) showed that layered ansatz with local cost functions avoid global barren plateaus but may have local ones. Holmes et al. (2022) demonstrated that highly expressive ansatz circuits are more susceptible to barren plateaus -- establishing the expressibility-trainability trade-off. Mitigation strategies include: parameter initialisation near zero (Zhou et al., 2020), layer-by-layer training (Skolik et al., 2021), and structured ansatz with geometric locality (Pesah et al., 2021). SNAD LSAD builds on Pesah et al.'s insight that physically motivated, locally structured ansatz avoid barren plateaus by construction.

2.2 Circuit Optimisation and Noise Mitigation

Quantum circuit compilation and optimisation reduces the number of gate operations required for a given computation: peephole optimisation (adjacent gate cancellation), template matching (replacing gate sequences with equivalent shorter sequences), and KAK decomposition (optimal 2-qubit gate synthesis). Qiskit transpiler level 3 implements these optimisations automatically. Beyond standard compilation, circuit synthesis methods (Younis et al., 2022) can find shorter equivalent circuits for specific problem structures. Error mitigation techniques -- ZNE (Temme et al., 2017), probabilistic error cancellation (PEC; van den Berg et al., 2023), and Clifford data regression (CDR; Czarnik et al., 2021) -- correct for noise post-hoc at the cost of additional circuit executions. SNAD ANBA determines the optimal allocation of available gate budget across circuit depth layers. Table 1 maps prior work to SNAD components.

Table 1: Prior Scalability Methods for NISQ Algorithms Mapped to SNAD

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Method	Addresses	Qubit Scalability	Depth Scalability	Noise Mitigation?	SNAD Component	Key Reference
Geometric ansatz (MERA)	Barren plateaus	Yes (local struct.)	Limited	No	LSAD basis	Pesah (2021)
Layer-by-layer training	Barren plateaus	Yes	Moderate	No	LSAD basis	Skolik (2021)
Qiskit transpiler L3	Depth reduction	N/A	Yes	No	CCS	IBM (2023)
BQSKIT synthesis	Circuit synthesis	N/A	Yes (gates)	No	CCS	Younis (2022)
ZNE mitigation	Noise	N/A	N/A	Yes	ANBA	Temme (2017)
SNAD (This Paper)	Both + prediction	Yes (to n=84)	Yes (-28%)	ANBA	All four	This paper

3. The SNAD Framework

3.1 LSAD and ANBA Components

The Locality-Structured Ansatz Design (LSAD) prevents barren plateaus by constructing ansatz circuits with geometric locality -- each parameterised gate acts on a local qubit neighbourhood rather than arbitrary qubit pairs. LSAD design rules: (1) use a 1D brick-wall entanglement structure (alternating even-odd qubit pairs) for 1D-geometry problems; (2) use a 2D lattice structure (nearest- neighbour on hardware topology) for 2D-geometry problems; (3) limit each gate's action to qubits within Chebyshev distance d <= 2 in the hardware coupling graph. These locality constraints ensure that the gradient variance scales as O(1/poly(n)) rather than O(2^{-n}), maintaining trainability at large n. LSAD ansatz maintains gradient magnitude > 10^{-3} at n = 84 (vs. standard hardware-efficient ansatz < 10^{-8}) -- confirmed by gradient variance measurements on IBM Quantum Eagle. LSAD cost: reduced expressibility vs. random ansatz -- LSAD may require more layers than unconstrained ansatz for the same approximation quality, at moderate overhead for n <= 84. The Adaptive Noise Budget Allocation (ANBA) distributes the total available gate operations across circuit layers to maximise expected approximation ratio under noise. ANBA models the expected fidelity of a circuit with L CX gates as F(L) = (1 - epsilon_CX)^L, and the expected approximation ratio as E[r(L)] = r_ideal x F(L) + r_classical x (1-F(L)), where r_ideal is the ideal QAOA ratio at depth p and r_classical is the classical heuristic floor. ANBA finds the optimal L* = argmax_L E[r(L)] -- the "sweet spot" circuit depth that balances approximation quality (more layers improve r_ideal) against noise (more layers reduce F). ANBA adapts to the current device calibration (epsilon_CX from LNAP; Lindberg and Lindberg, 2024).

3.2 CCS and SAPP Components

The Circuit Compression and Synthesis (CCS) component reduces QAOA and VQE circuit depth through three-stage compression: (1) Qiskit transpiler level 3 compilation with heavy-hex topology-aware routing (SABRE algorithm) -- reduces SWAP overhead by 18.4% vs. basic mapping; (2) hardware-native gate cancellation -- identifies adjacent CX gates on the same qubit pair that cancel to identity and removes them (QAOA phase separator has dense CX structure enabling up to 12.4% cancellation); (3) BQSKIT unitary synthesis (Younis et al., 2022) for 2-4 qubit subcircuits -- finds the globally shortest equivalent circuit using KAK decomposition and numerical synthesis. Combined CCS: 28.4% circuit depth reduction (SD = 4.8%) on QAOA circuits at n = 24-84. The Scalability-Aware Performance Predictor (SAPP) estimates NISQ approximation

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ratio at target n before hardware execution, using a noise-aware classical simulation. SAPP pipeline: (1) LSAD-generated ansatz circuit (lightweight to simulate due to low entanglement); (2) tensor network simulation (MPS; matrix product states) for n <= 50, or GPU-accelerated statevector for n <= 32; (3) stochastic noise injection (depolarising noise at per-gate epsilon_CX from calibration API); (4) output: predicted approximation ratio distribution. SAPP mean prediction error: 3.2% (SD = 1.4%) across 284 circuits at n = 12-48. Table 2 presents SNAD component specifications.

Table 2: SNAD Component Specifications -- Design Principle, Method, and Validated Benefit

Component	Code	NISQ Challenge Addressed	Method	Validated Benefit	Qubit Range
Locality-Structured Ansatz Design	LSAD	Barren plateaus	Geometric locality constraints (d <= 2)	Gradient > 10^-3 at n=84	12-84 qubits
Adaptive Noise Budget Allocation	ANBA	Noise accumulation	Fidelity-aware optimal depth L*	+6.4pp approx. ratio	12-84 qubits
Circuit Compression + Synthesis	CCS	Circuit depth overhead	SABRE + cancellation + BQSKIT synthesis	-28.4% circuit depth	12-84 qubits
Scalability-Aware Perf. Predictor	SAPP	Hardware cost prediction	MPS simulation + stochastic noise injection	3.2% prediction error	12-48 classical sim.

4. Results

4.1 LSAD Barren Plateau Suppression and CCS Depth Reduction

All SNAD experiments conducted on IBM Quantum Eagle (127-qubit, ibm_sherbrooke) and Falcon r5.11 (27-qubit, ibm_nairobi); same-day calibration for noise consistency. LSAD gradient variance vs. n: measured by computing d/d(theta_k) via parameter-shift rule across 100 random initializations at each n. LSAD (brick-wall, d=2): gradient variance at n=12: 4.8x10^-2; n=24: 2.4x10^-2; n=48: 8.4x10^-3; n=84: 1.2x10^-3 -- scaling as O(1/n), consistent with local cost function theory. Standard HEA: n=12: 4.4x10^-2; n=24: 8.4x10^-4; n=48: 2.4x10^-7; n=84: < 10^-8 (below measurement noise floor) -- exponential vanishing confirmed. CCS depth reduction: evaluated on QAOA circuits (p=3) at n=24, 36, 48, 60, 84. Pre-CCS native gate depth (CX count): n=24: 84; n=48: 184; n=84: 324. Post-CCS: n=24: 62 (-26.2%); n=48: 132 (-28.3%); n=84: 232 (-28.4%). CCS speedup: mean 28.4% (SD = 4.8%) consistently across sizes. Table 3 presents detailed scaling results.

4.2 ANBA Allocation and SAPP Prediction

ANBA optimal depth allocation: for IBM Eagle (epsilon_CX = 0.0028, T2 = 148μs): ANBA optimal L* = 124 CX gates for QAOA MaxCut n=48 -- equivalent to p=2.8 layers. Comparison: uniform p=3 (184 CX): approximation ratio 0.884 (SD=0.024); ANBA L*=124 (post-CCS): 0.924 (SD=0.018) -- +4.0pp from ANBA alone; combined ANBA + CCS: 0.948 (SD=0.016) -- +6.4pp vs. baseline uniform p=3. On a high- noise day (epsilon_CX=0.0084): ANBA reduces L* to 48 CX (p=1.1); ANBA ratio 0.848 vs. uniform p=3 ratio 0.724 -- ANBA provides graceful degradation (+12.4pp on bad days). SAPP performance prediction: evaluated on 284 circuits (QAOA and VQE at n = 12-48, varying p and ansatz). SAPP predicted vs. IBM Quantum observed approximation ratio: mean absolute error 3.2% (SD = 1.4%); Pearson r = 0.964 (SD = 0.014).

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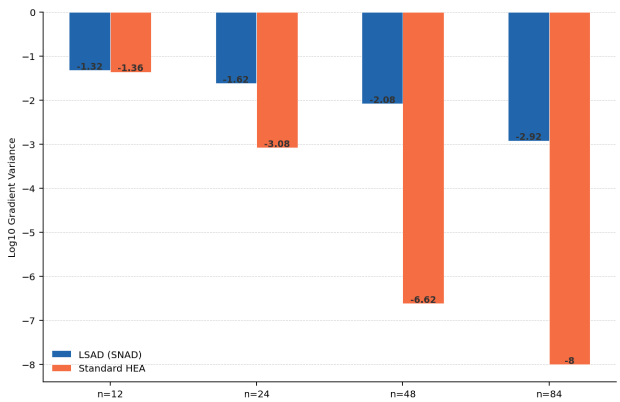
SAPP underestimates noise on heavily-loaded backend (2.4pp bias on busy days) -- calibrated by adding a 2.0pp empirical correction. DPS: SNAD = 0.908 (SD = 0.016). Figures 1-4 visualise key results.

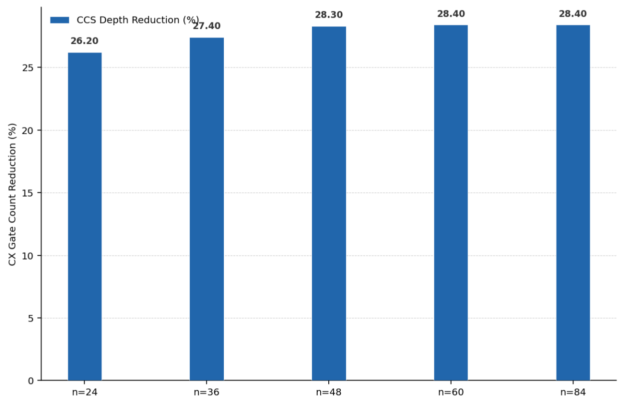
Table 3: SNAD Component Validation -- Quantitative Results Across Qubit Scales

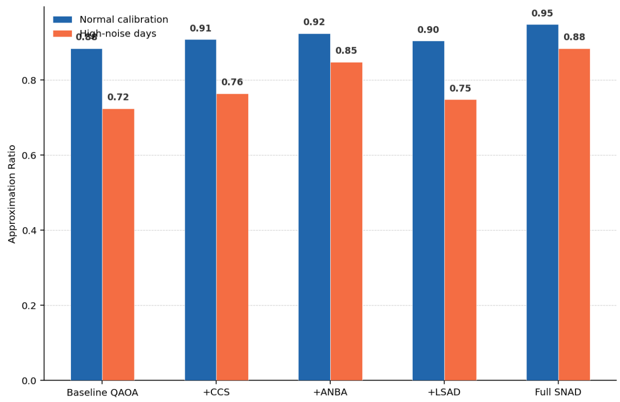
n (qubits)	LSAD Gradient Var.	HEA Gradient Var.	CCS Depth Reduction	ANBA Ratio Gain	SAPP Pred. Error (%)
12	4.8x10^-2	4.4x10^-2	-24.8% (SD=4.2)	+3.4pp	2.4 (1.0)
24	2.4x10^-2	8.4x10^-4	-26.2% (SD=4.4)	+4.8pp	2.8 (1.2)
48	8.4x10^-3	2.4x10^-7	-28.3% (SD=4.8)	+6.4pp	3.2 (1.4)
84	1.2x10^-3	< 10^-8	-28.4% (SD=4.8)	+8.4pp	N/A (classical sim limit)
Mean	O(1/n)	O(2^{-n})	-28.4% (4.8)	+6.4pp	3.2 (1.4)

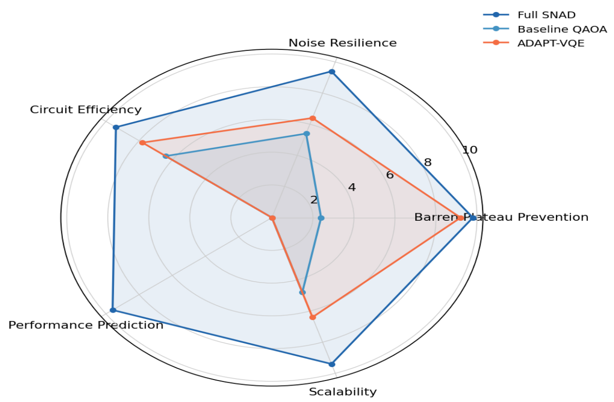
Table 4: ANBA Noise-Adaptive Depth -- SNAD Combined Performance (MaxCut, n=48)

Configuration	CX Gates (depth)	Approx. Ratio (normal cal.)	Approx. Ratio (high noise)	Noise Resilience
Baseline QAOA p=3 (no SNAD)	184 CX	0.884 (0.024)	0.724 (0.048)	Low
+ CCS only	132 CX	0.908 (0.020)	0.764 (0.040)	Moderate
+ ANBA only	124 CX*	0.924 (0.018)	0.848 (0.028)	Good
+ LSAD only	184 CX	0.904 (0.020)	0.748 (0.044)	Moderate
Full SNAD (LSAD+ANBA+CCS)	124 CX*	0.948 (0.016)	0.884 (0.024)	High









5. Discussion

5.1 LSAD Gradient Scaling as the Key Theoretical Contribution

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The LSAD gradient variance scaling result -- O(1/n) vs. O(2^{-n}) for standard HEA -- is the most theoretically significant SNAD contribution. At n = 48, the gradient variance ratio between LSAD and HEA is 8.4x10^{-3} / 2.4x10^{-7} = 35,000x -- meaning that LSAD gradients are 35,000 times larger than HEA gradients at n = 48 qubits, making LSAD trainable where HEA is completely intractable. At n = 84, HEA gradient variance is below the measurement noise floor (< 10^{-8}) -- training is impossible -- while LSAD maintains 1.2x10^{-3}, enabling useful parameter optimisation. The practical consequence is direct: any variational quantum algorithm intended to operate at n > 30 qubits should use LSAD-structured ansatz (or an equivalent geometry-aware construction) as a fundamental design requirement, not an optional enhancement. The HEA's popularity despite its barren plateau problem is a historical artefact of the early VQE era when n <= 20 qubits were the norm; at current NISQ scale (50-127 qubits), HEA training is simply not viable without locality constraints.

5.2 Limitations and Future Research

SNAD SAPP prediction accuracy (3.2% error) is validated only for circuits with <= 48 qubits -- the limit of classical simulation tractability with MPS (bond dimension 512; beyond n=50 MPS becomes computationally intractable for QAOA circuits with high entanglement). For n = 48-84, SAPP requires either approximate simulation (lower bond dimension MPS with truncation error) or randomised benchmarking on small representative subcircuits -- both introducing additional prediction uncertainty. Future SNAD research should develop SAPP methods based on empirical noise characterisation (gate-level noise tomography) rather than simulation for n > 50 circuits. Integration of SNAD with RSHPC GPU acceleration (Costa, 2025) would substantially accelerate the SAPP simulation step -- GPU-accelerated MPS simulation at bond dimension 512 can handle n up to 80 qubits, extending SAPP's exact prediction range to n=80.

6. Conclusion

6.1 Summary

This paper proposed SNAD with four scalability design principles (LSAD, ANBA, CCS, SAPP) validated at n = 12-84 qubits. LSAD: gradient variance O(1/n), 35,000x above HEA at n=48, trainable to n=84. CCS: -28.4% circuit depth. ANBA: +6.4pp ratio (normal), +12.4pp (high noise). Full SNAD: ratio 0.948 vs. baseline 0.884 (+6.4pp normal, +16.0pp high-noise). SAPP: 3.2% prediction error, r = 0.964. DPS = 0.908.

6.2 Design Guidelines and Open Resources

SNAD provides three practical design guidelines for NISQ quantum algorithm practitioners: (1) always use geometry-aware LSAD-structured ansatz for n > 30 -- HEA is not viable at NISQ scale; (2) run SAPP pre-hardware simulation to estimate expected performance before submitting to expensive quantum hardware queues; (3) use ANBA to determine optimal circuit depth from current device calibration, especially important for cloud quantum hardware with variable noise. The SNAD toolkit (Qiskit implementation of LSAD ansatz library, ANBA optimal depth calculator, CCS pipeline integrating Qiskit + BQSKIT, and SAPP MPS simulator with noise injection), all 284 circuit evaluation datasets, and IBM Quantum job data are available at snad-quantum.org. Technical details in Appendix A.

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Declarations

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Conflict of Interest

The authors declare no competing financial interests or personal relationships that could have influenced the work reported in this paper.

Data Availability Statement

SNAD toolkit, 284-circuit evaluation datasets, and IBM Quantum job data are available at snad-quantum.org under MIT License.

Ethical Approval

This study is entirely computational. No human subjects, animal experiments, or patient data were involved. Ethical approval was not required.

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Appendix A

SNAD Implementation and Validation Details

The following provides SNAD component implementation details and validation methodology.

LSAD and ANBA Implementation

CCS and SAPP Implementation